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## IN THE UNITEDISTATES PATENT AND TRADEMARK OFFICE

In re application of: Salvatore N. Storino, et al. Date: February 20, 2003

Group Art Unit:

2823

**IBM** Corporation

Examiner:

K. Nguyen

Intellectual Property Law

Serial No.:

09/751,163

Dept. 917, Bldg. 006-1

Filed:

December 29, 2000

3605 Highway 52 North

Title: METHOD OF ELIMINATION OF

PARASITIC BIPOLAR ACTION IN SILICON ON INSULATOR (SOI) DYNAMIC LOGIC CIRCUITS

Rochester, MN 55901

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**Assistant Commissioner for Patents** 

Washington, D.C. 20231

MAR 0 6 2003

OFFICE OF THE SPECIAL **PROGRAMS EXAMINER** 

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February 20, 2003

(Date of Deposit)

Lisa M. Plank

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Identification Of Person(s) Making This Disclaimer

TERMINAL DISCLAIMER **APPROVED** 

I,

Name of disclaimant:

Grant A. Johnson

Residing at:

**IBM Corporation** 

Intellectual Property Law Dept.

Dept. 917, Bldg. 006-1

3,605 Highway 52 North

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Rochester, Minnesota 55901-7829

Docket No.: Serial No.:

RO998-200B 09/751,163

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represent that I am  an inventor of this in an assignee of this in a representative authorized below.	/
Identity of Assignee and Title of Dis	sclaimant (if applicable)
The assignee is:	
Name of assignee: Address of assignee: Title of disclaimant:	International Business Machines Corporation Armonk, New York 10507 Attorney, Intellectual Property Law authorized to sign on behalf of assignee
Recordal of Assignment in PTO	
_X_ The assignment was recorded Reel <u>9813</u> Frame <u>0702</u> Authorization for recordal of	d on March 8, 1999.  f the assignment is separately attached
Extent of Interest	
The extent of my (our) interest is in  the whole of this inverse a sectional interest in disclaimant(s):	this invention as follows (here state the exact interest of the
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application, which would extend be reference to premature expiration by of invalidity or any other unforeseer  X United States Patent disclaimer,	al part of any patent granted on the above-identified yond the expiration date of the full statutory term, without y reason of non-payment of maintenance fees, a court holding nevent, of:  No. 6,271,686, as presently shortened by any terminal on application number 0 /,
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